

74LS273 Octal D-Type Flip-Flop Register IC



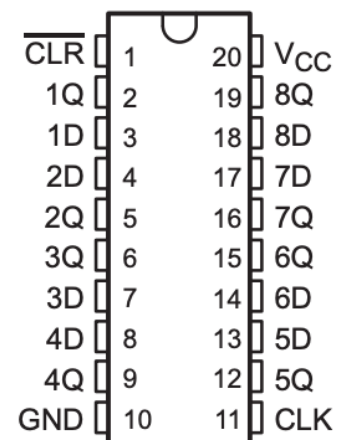
Features:

The 74LS273 is an octal D-type flip-flop implemented using TTL logic with positive edge triggering and a direct clear input. Each flip-flop has an independent data input and shares a common clock and clear control. Data present at the inputs is transferred to the outputs on the rising edge of the clock, while the clear input forces all outputs low when activated. The device is optimized for use in buffer storage registers, shift registers, and digital data handling systems with reliable high speed operation.

Specifications	
Supply Voltage (VCC)	4.75V to 5.25V
Input HIGH Voltage (VIH)	2.0V min
Input LOW Voltage (VIL)	0.8V max
Output HIGH Voltage (VOH)	2.7V min
Output LOW Voltage (VOL)	0.5V max
Output Current High (IOH)	-0.8 mA
Output Current Low (IOL)	16 mA
Max Clock Frequency	30 MHz typical 40 MHz max
Power Dissipation	10 mW per flip flop
Operating Temperature	0C to 70C
Package Type	20-pin PDIP
Usage	Register, buffer storage, shift register, pattern generator

Pinouts:

Pin Name	Type	Description
CLR	Input	Clear input active low
1Q	Output	Output flip-flop 1
1D	Input	Data input flip-flop 1
2D	Input	Data input flip-flop 2
2Q	Output	Output flip-flop 2
3Q	Output	Output flip-flop 3
3D	Input	Data input flip-flop 3
4D	Input	Data input flip-flop 4
4Q	Output	Output flip-flop 4
GND	Power	Ground
CLK	Input	Clock input
5Q	Output	Output flip-flop 5



5D	Input	Data input flip-flop 5
6D	Input	Data input flip-flop 6
6Q	Output	Output flip-flop 6
7Q	Output	Output flip-flop 7
7D	Input	Data input flip-flop 7
8D	Input	Data input flip-flop 8
8Q	Output	Output flip-flop 8
VCC	Power	Supply voltage

Product Pictures:

