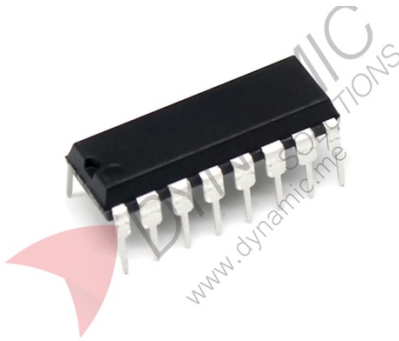


74LS161 4-Bit Binary Synchronous Counter IC



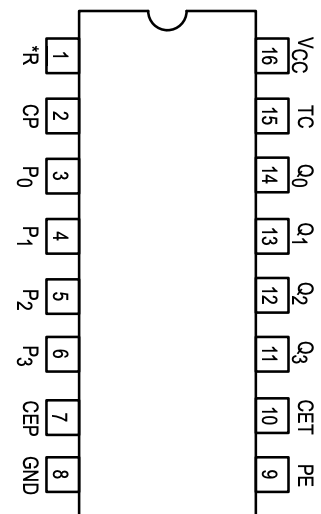
Features:

The 74LS161 is a 4-bit synchronous binary counter designed with edge-triggered flip-flops and synchronous parallel loading capability. It features dual count enable inputs for flexible control and cascading without additional gating. The device includes a fully decoded terminal count output and an asynchronous master reset that overrides all other inputs. It is widely used in counting systems, frequency division, and digital control logic applications.

Specifications	
Supply Voltage (VCC)	4.75V to 5.25V
Input HIGH Voltage (VIH)	2.0V min
Input LOW Voltage (VIL)	0.8V max
Output HIGH Voltage (VOH)	2.7V min
Output LOW Voltage (VOL)	0.5V max
Output Current High (IOH)	-0.4 mA
Output Current Low (IOL)	8 mA
Max Clock Frequency	35 MHz
Operating Temperature	0C to 70C
Count Range	0 to 15
Usage	Counters, frequency division, memory addressing

Pinouts:

Pin Name	Type	Description
MR	Input	Master reset active low
CP	Input	Clock input
P0	Input	Parallel data input bit 0
P1	Input	Parallel data input bit 1
P2	Input	Parallel data input bit 2
P3	Input	Parallel data input bit 3
CEP	Input	Count enable parallel
CET	Input	Count enable trickle
PE	Input	Parallel enable active low
Q0	Output	Output bit 0
Q1	Output	Output bit 1
Q2	Output	Output bit 2
Q3	Output	Output bit 3



TC	Output	Terminal count output
GND	Power	Ground
VCC	Power	Supply voltage

Product Pictures:

