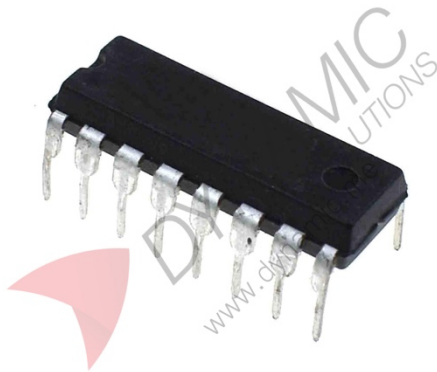


74HC173 Quad D-Type Flip-Flop 4-Bit Register IC

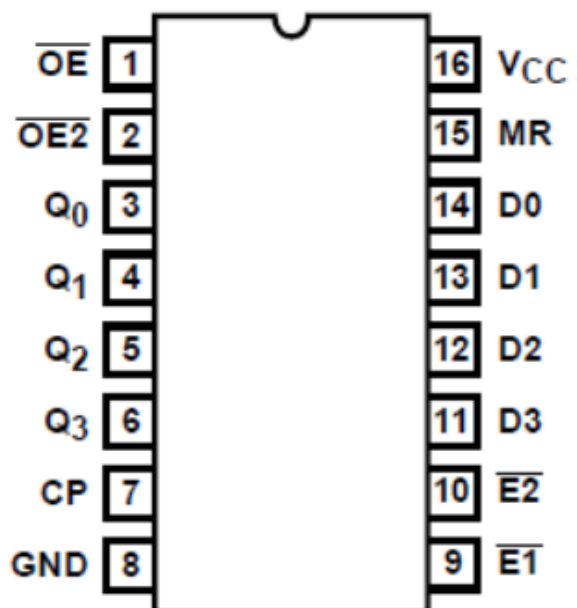


Features:

The 74HC173 is a high speed CMOS quad D-type flip-flop featuring three-state buffered outputs and synchronous operation from a common clock. The device includes gated input and output enable controls allowing data retention without clock interruption. It provides high drive capability suitable for bus oriented systems and supports direct interfacing with LSTTL logic. With low power consumption and balanced propagation delay, it is widely used in registers, memory storage, and digital control circuits.

Specifications	
Supply Voltage (VCC)	2V to 6V (HC), 4.5V to 5.5V (HCT)
Input Voltage	0 to VCC
Output Voltage	0 to VCC
Output Current per Pin	+25 mA
Continuous Current	+70 mA
Operating Temperature	-55C to 125C
Storage Temperature	-65C to 150C
Package Type	PDIP, SOIC, TSSOP
Usage	Register, bus interface, data storage

Pinouts:



Pin Name	Type	Description
OE	Input	Output enable control
OE2	Input	Output enable control
Q0	Output	Data output bit 0
Q1	Output	Data output bit 1
Q2	Output	Data output bit 2
Q3	Output	Data output bit 3
CP	Input	Clock input
GND	Power	Ground
E1	Input	Input enable
E2	Input	Input enable
D3	Input	Data input bit 3
D2	Input	Data input bit 2
D1	Input	Data input bit 1
D0	Input	Data input bit 0
MR	Input	Master reset
VCC	Power	Supply voltage

Product Pictures:

